

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Frank Dumont, Chee Lam Tan, Yuan Fuat Chin and
Andrzej Ziolkowski
Filed: Herewith
For: Video Apparatus, Notably Video Recorders, and
Processes for Use in Said Video Apparatus

PRELIMINARY AMENDMENT

Hon. Commissioner for Patents
Washington, D.C. 20231

Sir:

Please amend the above identified application as follows.

In the specification:

Page 6, line 24, delete section heading and replace with

--DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS--.

Page 11, line 22, delete paragraph and replace with,

--The video decoder 28 comprises filters, some of which are used for the above-mentioned separations of signals (for instance a comb-filter for luminance/chrominance separation), other ones allowing video signal processing such as chroma saturation control or luminance contrast and brightness control. The filters are adaptive, which means that their parameters are controlled by the micro-processor 26, through a serial I²C-bus (represented by a dotted line).--

Page 13, line 28, delete paragraph and replace with,

--The multiplexer 42 is also connected to both the input and the output of the bit-stream processor 32. Alternatively, the connections between the multiplexer 42 and the bit-stream processor 32 could be implemented through a bi-directional link (as data processed in the bit-stream processor 32 can only go one way at a time, no simultaneous reading and recording on a tape being possible).--

Page 15, line 31, delete paragraph and replace with,

--In a second period of time, the user activates playback (PB) of a video tape. The VCR will then reproduce the content of the tape according to the memorised preferences. This consists for the micro-processor 26 mainly in:

(c) reading parameters in the memory 56;

(d) controlling the analog unit 14, the digital unit 16, the path switch 22 and the monitor switch 18 accordingly.--

Page 19, line 4, delete paragraph and replace with,

--The output of the digital unit 16 is also transmitted to a second input of the path switch 48. This allows, when the path switch 48 is in the corresponding position, an analog signal from the analog source 12 to go through the digital unit 16 before being recorded in the analog unit 14, for instance according to the VHS or S-VHS standard. The recording will therefore have a better quality thanks to signal processing realised in the digital unit 16, as already explained.--

In the claims:

Page 27, line 11, delete claim 18 and replace with

--18. (Amended) A process according to claim 16, further including the step of reading signal processing parameters in a memory of the video apparatus which content can be changed by the user.

21. (Amended) A process for controlling a micro-processor of a video apparatus according to claim 3, comprising the steps of:

- instructing an on-screen display processor to display a menu;
- waiting for an information from the user;
- storing said information in a memory;
- controlling the path switch depending on said information.--

In the Abstract:

Page 30, delete in total and replace with,

--ABSTRACT

A video apparatus has a first circuit generating a first baseband analog video signal on a first output. A second circuit at least connectable to the first output, can digitize the first baseband analog video signal and process and output a corresponding digital stream on a second output. The second output is in turn at least connectable to a third circuit generating on a third output a second baseband analog video signal on the basis of the digital stream.--

Marked version to show changes made**In the specification:**

Page 6, line 22, amend section heading as follows

DETAILED DESCRIPTION OF [PREFERED] PREFERRED EMBODIMENTS

Page 11, line 22, delete paragraph and replace with,

The video decoder 28 comprises filters, some of which are used for the above-mentioned separations of signals (for instance a comb-filter for luminance/chrominance separation), other ones allowing video signal processing such as chroma saturation control or luminance contrast and brightness control. The filters are adaptive, which means that their parameters are [controled] controlled by the micro-processor 26, through a serial I²C-bus (represented by a dotted line).

Page 13, line 22, amend paragraph as follows,

The multiplexer 42 is also connected to both the input and the output of the bit-stream processor 32. Alternatively, the connections between the multiplexer 42 and the bit-stream processor 32 could be implemented through a [bi-directionnal] bi-directional link (as data processed in the bit-stream processor 32 can only go one way at a time, no simultaneous reading and recording on a tape being possible).

Page 15, line 31, amend paragraph as follows,

--In a second period of time, the user activates playback (PB) of a video tape. The VCR will then reproduce the content of the tape according to the memorised preferences. This consists for the micro-processor 26 mainly in :

(c) reading parameters in the memory 56 ;

(d) [controling] controlling the analog unit 14, the digital unit 16, the path

Page 19, line 4, amend the paragraph as follows,

The output of the digital unit 16 is also transmitted to a second input of the path switch 48. This allows, when the path switch 48 is in the corresponding [poisition] position, an analog signal from the analog source 12 to go through the digital unit 16 before being recorded in the analog unit 14, for instance according to the VHS or S-VHS standard. The recording will therefore have a better quality thanks to signal processing realised in the digital unit 16, as already explained.

In the claims

18. (Amended) A process according to claim 16 [or 17], further including the step of reading signal processing parameters in a memory of the video apparatus which content can be changed by the user.

21. (Amended) A process for controlling a micro-processor of a video apparatus according to claim 3 [or 8], comprising the steps of:

instructing an on-screen display processor to display a menu;
waiting for an information from the user;
storing said information in a memory;
controlling the path switch depending on said information.

REMARKS

These amendments are submitted to correct spelling errors and to delete multiple dependency in some claims.

February 12, 2001

Respectfully submitted,
Frank Dumont, et al.

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